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Class	S
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U.S. **UTILITY** Patent Application

**O.I.P.E.**

PATENT DATE

SCANNED

## Q.A

APPLICATION NO. 09/874307	CONT/PRIOR	CLASS 708	SUBCLASS 495	ART UNIT 2121	EXAMINER J. H. 2
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## APPLICANTS

Graham Kirsch

**TITLE**

Method and circuit for alignment of floating point significands in a SIMD array MPP

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____			<b>ISSUE FEE</b>	
_____	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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